

A Network-Computing Infrastructure for Tool Experimentation Applied to Computer Architecture Education*

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Abstract

Computer architects increasingly depend on the use of software tools to evaluate and investigate the design of computer systems. It is therefore very important that educators in this field promote extensive tool-based experimentation by students in architecture classes. However, the integration of today's complex architecture tools into curricula poses several challenges to an instructor, including management of powerful computing resources, software installation and maintenance, and development of tool-specific educational material. This paper describes how these challenges are addressed by a universally accessible network-computing infrastructure — NETCARE — that provides educators with a Web portal to access computing resources, executable tools and educational material.

1 Introduction

As the complexity of computing systems continues to grow, computer-aided design (CAD) software has become an essential tool to computer architects in both industry and academia. Prospective employers expect graduating computer engineering students to be proficient in the usage (and development) of tools; therefore,

computer architecture educators must promote extensive experimentation by students with CAD tools in school.

Computer architecture tools, such as simulators and compilers, are complex and often demand powerful computing resources to deliver acceptable performance levels. Furthermore, tools tend to be tailored to specialized architecture aspects, such as micro-architecture, memory hierarchy, multi-processors, operating systems and compilers. A typical undergraduate- or graduate-level architecture discipline covers many such topics, and is likely to require the availability of several tools for CAD experiments.

Educators face many obstacles in the deployment of an architecture tool for experimentation in a class environment. They must (1) obtain access to hardware resources that meet a tool's system requirements, including student accounts; (2) install and maintain the tool (software and documentation); (3) develop education content (tutorials, assignments) to be used in class. If many tools are intended to be used throughout the semester, the overheads are magnified in proportion to the number of needed tools. Furthermore, it becomes important to (4) guarantee that tools are presented to students with user-friendly interfaces. While commercial tools often provide rich graphical user interfaces, many leading-edge architecture tools are developed as a result of research efforts and provide text-based interfaces. Such interfaces, while being efficient for expert users, are an additional overhead to a novice user.

At the authors' universities, several tools are being incorporated in courses by using an infrastruc-

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ture for computer architecture education that leverages network-computing technology to provide a Web portal to tools. The infrastructure is based on the Purdue University Network Computing Hubs [8] and is currently available as part of the NETwork-computer for Computer Architecture Research and Education (NETCARE), a three-university consortium consisting of Purdue University, Northwestern University and the University of Wisconsin.

The infrastructure provides access to large pools of heterogeneous hardware resources, promotes reusability of software installations, documentation and educational content, and provides standard Web-based user interfaces. This paper describes how each of the above-mentioned obstacles involved in the integration of tools into courses are addressed by the infrastructure, and reports on experiences of the use of the system for architecture education.

2 Underlying resources

Computer architecture software tools demand powerful computing resources. A detailed, cycle-accurate execution-driven architectural simulation of a micro-processor usually takes hours to complete on a high-end server or workstation. Often, many data points are required to draw conclusions from an experiment. In addition, different tools often require different combinations of target architectures and operating systems. Providing access to plentiful computing resources to students adds to the overhead of setting up tool-based experiments in architecture classes. It is also possible that the instructor's institution does not own the high-end or specialized hardware resources that are required to run the desired tools.

The network-computing infrastructure leveraged by NETCARE addresses this administrative overhead by providing (1) resource-management mechanisms for a set of execution servers, which may consist of heterogeneous machines distributed across administrative domains, and (2) access to these resources without the need to set up individual user accounts on each server.

The PUNCH infrastructure allows NETCARE users to access a large pool of computers via "shadow" accounts. Shadow accounts are conventional (e.g. Unix) user accounts that are created on behalf of PUNCH on an execution server when the server is added to the system's resource pool. After this initial setup, no individual user accounts need to be created on the server: a shadow account is allocated to a NETCARE user dynamically for each tool execution request, and de-allocated upon completion. This mechanism provides immediate access to computing resources for new NET-

CARE users, and automatic access to newly added computing resources to the existing user base.

The resource-management subsystem of PUNCH incorporates load-balancing and predictive performance modeling mechanisms [9], and provides access to cluster management software such as Condor [12]. This allows the system to provide access to a large number of hardware resources through a single entry point. Currently, the NETCARE infrastructure provides access to 5 dedicated servers at Purdue, and approximately 600 Condor nodes at Wisconsin and 100 nodes at Purdue.

3 Available computer architecture tools

Research performed in academia and industry continues to provide public-domain and commercial tools for the design, evaluation and programming of computing systems. These tools provide valuable material for experimentation in architecture disciplines. However, due to their complexity, such tools most often demand time-consuming installation and maintenance procedures.

The NETCARE infrastructure addresses the tool installation, documentation and maintenance overheads by promoting reusability. The addition of a tool to NETCARE still may demand complex installation procedures; however, once installed, it automatically becomes available to users across different administrative domains. Furthermore, tool and/or documentation maintenance needs to be performed only for the single installation site.

The installation of a tool onto NETCARE involves two steps. First, the tool is installed onto suitable resources following its original installation procedures. NETCARE provides access to *unmodified* program binaries; hence, tools can be installed without requiring access to their source code or object files. The second step involves the creation of a Web user interface to the tool's execution and documentation. Section 5 describes how the underlying PUNCH architecture supports both native (graphical user-interface — GUI) and customized (HTML-based) interfaces to tools.

Many representative research tools in computer architecture and parallel programming have been installed onto NETCARE as part of this project. These tools, corresponding documentation, and educational material are being integrated into computer education curricula.

Table 1 shows the set of computer architecture and parallel programming tools currently available on NETCARE. For each tool, the table lists its user inter-

Tool	User interface
CacheSim5 [4]	HTML forms
<i>Dinero-IV</i>	HTML forms
DLXView [6]	X Window GUI
HPAM.Sim [1]	HTML forms
LSU Proteus [2]	HTML forms
<i>RSIM</i> [14]	HTML forms
Shade [4]	HTML forms
<i>SimpleScalar</i> [3]	HTML forms
WWT-II [13]	HTML forms
XSPIM [6]	X Window GUI
MaxP [11]	HTML forms
Polaris [17]	HTML forms
Trimaran [7]	X Window GUI
Ursa Minor [15]	X Window GUI

Table 1: Computer architecture and parallel programming tools currently available on NETCARE. Tools in *italics* are currently relinked to run on Condor.

face: for tools with native graphical/interactive user interfaces, NETCARE provides the original interface to the user through remote display technologies such as Virtual Network Computing (VNC [16]). For tools with text-based interfaces, NETCARE provides a customized interface based on HTML forms (Section 5).

4 Educational content

In addition to providing access to hardware and software resources, integrating tools to existing computer engineering classes requires the availability of extensive educational material. This includes manuals, tool documentation, answers to frequently-asked questions, examples, tutorials and homeworks.

Educational content in NETCARE is available as a collection of on-line documents accessible to students and instructors via the web-based interface (Figure 1), including:

1. *Original tool documentation.* All documentation that is provided in the tool’s distribution package.
2. *Local tool documentation.* Includes description of extra features and/or limitations that are specific to the interface of the tool provided by NETCARE.
3. *Questions and Answers.* Answers to frequently-asked questions (FAQs).

4. *Educational modules and examples.* Include example files, first-time user’s guide (tutorials introducing the tool’s interface), and educational modules (tutorials introducing the tool’s capabilities and functionality).
5. *Exercises.* Documents with tool-based experiments (exercises and open-ended projects) that can be assigned in classes.

Items 4 and 5 are designed to facilitate the integration of tools with computer engineering courses. Educational modules are introductory tutorials on the use of a tool; their purpose is to guide the student through experiments that illustrate the tool’s functionality. Educational modules are self-contained documents that allow students to become familiar with a tool by independently experimenting with it and checking results and conclusions against the expected ones described in the module. Students are not expected to turn in assignments based on educational modules, but rather to familiarize themselves with the tool(s) that are used in exercises and course projects.

Exercises and open-ended projects are documents that are intended to be used in (graded) class assign-

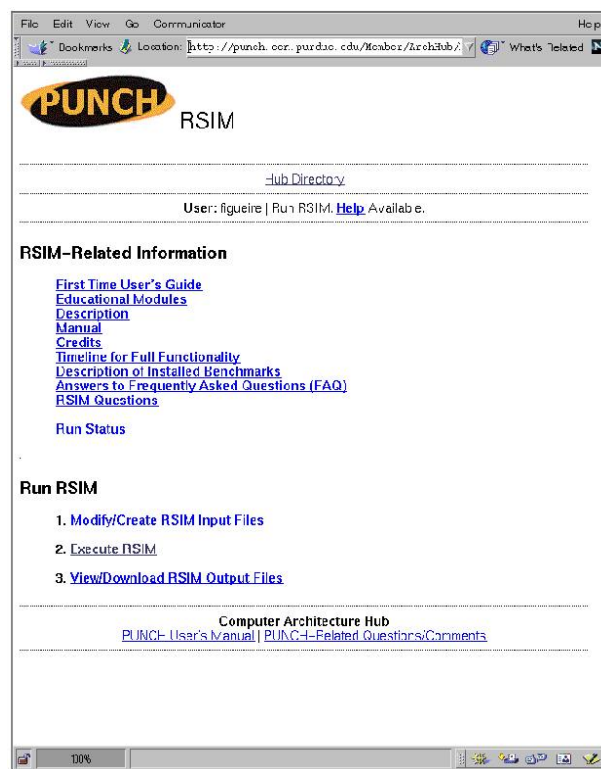


Figure 1: Access to RSIM’s execution, documentation and educational content.

ments. They assume that students have acquired the necessary background knowledge of the subject and related tool(s) from class material and the educational modules, and typically involve more challenging experiments than the ones of introductory educational modules.

The structure of educational modules and exercises/projects is similar; they differ on the scope/complexity of the experiments and on the availability of results, answers and conclusions to students. These documents contain five sections, as follows:

1. Introduction. Brief introduction to the problem being addressed by the module or exercise.
2. Experimental methods and models. It defines which aspects of the material discussed in the *Introduction* are covered by the experiment (and modeled by the tool used in the experiment), and explains the experimental methodology. In open-ended projects, the definition of methods and models may be left as part of the student's assignment.
3. Experiment setup. This section specifies the model parameters that will be varied/observed by the student during the experiment and the specific tool options to be used. As in *Experimental methods and models*, the definition of these parameters may be left as part of the student's assignment for open-ended projects.
4. Analysis. This section provides guidelines for the analysis of the data gathered after the experiments have been conducted. The purpose of this section is to aid in the interpretation and understanding of the results obtained from the experiment. For educational modules, results, answers and conclusions are provided in this section to allow students to check their work. For exercises and projects, results and answers are provided in a separate document whose access is granted by PUNCH to registered instructors only.
5. References. This section contains bibliographical references to all published material that is cited throughout the entire document.

5 User interfaces

The PUNCH infrastructure leveraged by NETCARE currently supports two types of user interfaces: X Window GUI and HTML forms. The former is used for

tools that have native graphical interfaces, while the latter is used for text-based tools. Both interfaces are provided to NETCARE users via conventional Web browsers: the graphical X Window interface is handled via a Java VNC [16] browser applet, while the text-based interface uses standard HTML language.

Many tools that can be used in computer architecture education are text-based (Table 1); often, these tools are configured via command-line parameters and files that are unfriendly to a novice user. PUNCH supports the definition of metaprograms that generate a dynamic HTML interface to these tools.

The metaprograms generate customized HTML interfaces and are specified via a high-level scripting language [10]. Metaprograms allow the translation of data obtained from the HTML interface (e.g. text boxes and radio buttons) into a format that is suitable for the tool's native interface (e.g. command-line parameters). Figure 2 shows an example of one of the dynamic HTML pages generated for the interface to the Dinero-IV simulator.

For a complex tool, the specification of metaprograms often require that its installers have good knowledge of its native interface and functionality, and hence

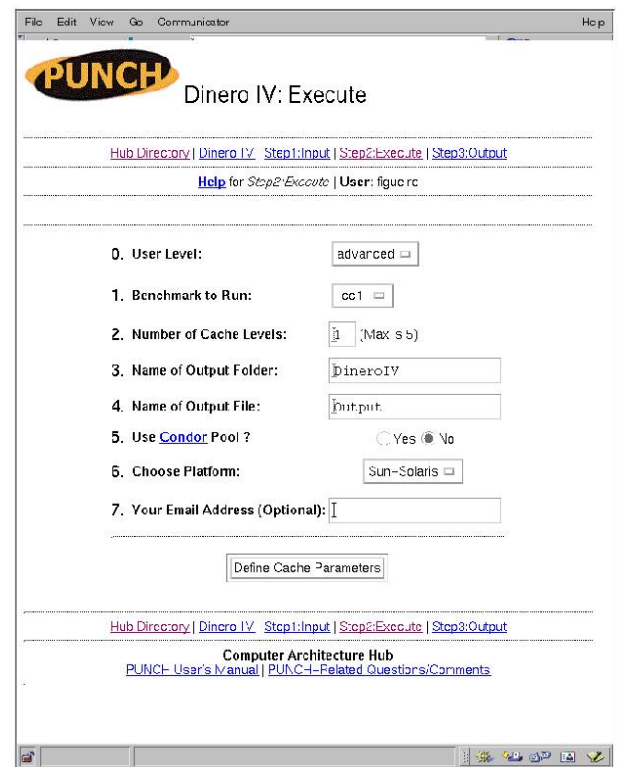


Figure 2: Entry-point HTML interface to execution of a Dinero-IV simulation.

requires extra programming effort in the process of Web-enabling a tool. However, once a metaprogram is developed for a complex tool, it can be re-used for the installation of the tool at different sites and used as a basis for the installation of newer versions/variants of the tool, or tools that exhibit similar functionality. Furthermore, the interface provided by metaprograms allows tailoring of the interface's complexity based on the needs of target users. For instance, the NETCARE metaprogram for the cache simulator Dinero-IV provides interfaces to both novice and advanced users. The former presents only basic cache simulation parameters to the student (e.g. size and associativity), while the latter includes parameters that may not be covered in an introductory memory hierarchy class (e.g. prefetch distance and fetch policies).

The definition of metaprograms for text-based tools that have simple interfaces and for X Window GUI tools can be performed with little overhead by reusing existing templates. Ongoing efforts include automatic generation of metaprograms for such types of tools.

6 Integration with curricula

The computer architecture and programming tools that are currently available via NETCARE have been used in several undergraduate and graduate classes across Purdue University, Northwestern University and Chicago State University since 1998. Tools have been used in experiments involving memory hierarchies (Cachesim5 and Dinero-IV), pipelining (DLXview), instruction sets and execution-based simulation techniques (Shade), parallel programming (Polaris and MaxP) and instruction-level parallelism (SimpleScalar) [5].

The following two subsections present examples of an educational module and an experimentation-based exercise using NETCARE's computing infrastructure. These educational documents are summarized in this paper; their complete versions are accessible from the Cachesim5 and WWT main pages in NETCARE (<http://www.ece.purdue.edu/NETCARE>).

6.1 Educational module example

The educational modules described in this subsection illustrate the use of a multiprocessor simulator (WWT-2) to study the parallel performance of both bus-based symmetric multiprocessors (SMPs) and distributed shared-memory multiprocessors (DSMs). The educational modules consist of two HTML documents, formatted as described in Section 4.

The *Introduction* section of each module briefly describes the multiprocessor architecture under study (SMP or DSM), the simulator (WWT-2) and the SPLASH-2 parallel benchmarks that are used in the experiment.

The *Experimental models* section describes the multiprocessor machine models supported by the simulator. The SMP module describes the bus-based multiprocessor with single-level snooping caches supported by WWT-2, while the DSM module shows that WWT-2 allows the modeling of DSMs consisting of SMP nodes and Simple-COMA cache coherence. The *Experimental setup* section describes the benchmarks used in the experiments (FFT, LU, Radix) and determines the simulation parameters that the student will control (e.g. number of processors varying from 1 to 16) and those that are fixed (e.g. cache size and processor speed).

The *Analysis* section provides the student with questions to guide their analysis of the experimental data gathered from the simulations (e.g. "What are the average parallel speedups and efficiencies for 4, 8 and 16 processors?"). Since educational modules are non-graded self-contained documents that are used to illustrate the usage of a tool, this section also contains answers to the proposed questions. The questions and answers provided in the module allow students to check their experimental data and conclusions. Figure 3 shows a screenshot of the answers provided in this section, including simulation results and a parallel speedup plot.

6.2 Exercise example

The example exercise described in this subsection has been used as a graded assignment in an undergraduate-level computer architecture class (CPTR-303) at Chicago State University. The topic covered by the assignment is memory hierarchy, and the tool used in the simulation experiments is Cachesim5. The assignment consists of two documents formatted as described in Section 4; the first exercise deals with hierarchies with a single cache level, while the second exercise deals with two-level cache hierarchies.

The *Introduction* section of the exercise documents contain a brief description of caches, referencing the class textbook's chapter on memory hierarchy. It also contains a description of the selected tool (Cachesim5), including a brief explanation of the execution-based simulation technique employed by it, and of the three Spec92 benchmarks (Ora, Doduc, Su2cor) used in the experiments.

The *Experimental methods* and *Experimental setup* sections describe the experiments that students are re-

quired to perform in the assignment: they must select three different cache organizations, simulate the execution of all benchmarks for each organization, and identify and summarize cache statistics (such as miss rates). The choices of cache organizations are constrained by a set of acceptable parameters (such as cache and block sizes and associativity) defined in the document.

The *Analysis* section contains several questions to be answered based on the experimental data collected from the simulations. As opposed to the educational modules of the previous subsection, no answers are provided to the students: their work will be graded based on their results and analysis. The students are also asked to make a recommendation as to which of the chosen cache organizations yields the best performance for the benchmarks under study (in terms of average miss rates) based on the simulation analysis.

6.3 Evaluations

The authors have surveyed students that had been exposed to Web-based experiments with tools in computer architecture classes. The survey consists of two questionnaires handed out to students in class: the first survey is distributed prior to the use of NETCARE

and asks students about previous experiences with conventional simulators; the second survey is distributed at the end of the semester, and asks students about their experience with Web-based tool experimentation. The motivation for the use of two surveys is to identify whether tools with Web-based interfaces are easier to learn and use than their native, command-line counterparts, and to provide feedback on how to improve the system's usability.

Feedback received from student evaluations indicates that the network-computing interface is easy to learn and an effective aid to understanding architecture concepts [5]. Most criticism was received from advanced users in graduate-level courses with regard to the HTML-based file manipulation; for this class of users, a Unix-like terminal interface can enhance productivity. We are currently looking into the integration of a shell interface to the network-computing infrastructure to address the needs of expert users who use tools for both research and education.

7 Using NETCARE

The educational infrastructure provided by NETCARE described in this paper is publicly accessible and available for use by educators, upon request. The system is accessible through any Web browser via the URL <http://www.ece.purdue.edu/NETCARE>. This entry point to the infrastructure allows educators and students to request accounts, access documentation, execute tools and contact the personnel involved in the project.

In addition to reusing the currently available infrastructure, NETCARE also allows educators to add and share tools and documents currently used in their courses to the infrastructure. New tools can be added via the the specification of metaprograms; templates for X Window-based tools and installation guidelines for text-based tools are available for reuse.

8 Conclusions

A large percentage of computer engineering graduates will need experience with computer-based simulation and design tools in their jobs. This paper presents a network-computing infrastructure — NETCARE — that provides universal access to tools and simulation-based experimentation in computer architecture courses.

The paper describes the current status of NETCARE, and presents a summary of the experiences obtained from its application to architecture education.

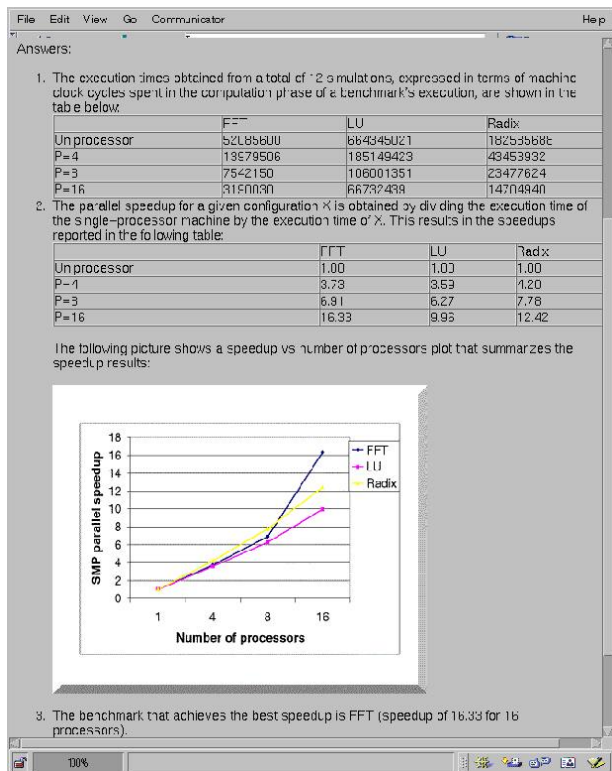


Figure 3: Parallel speedup plot of the SMP educational module for WWT-2.

The use of the infrastructure in the universities participating in the consortium has shown that it is able to reduce the overheads in hardware, software and documentation management, facilitating the integration of tools into existing computer architecture courses.

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